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(54) **Linear scalable FFT/IFFT computation in a multi-processor system**

(57) This invention relates to a linear scalable method for computing a Fast Fourier Transform (FFT) or Inverse Fast Fourier transform (IFFT) in a multiprocessing system using a decimation in time approach. Linear scalability means, as the number of processors increases by a factor P (for example), the computational cycle reduces by exactly the same factor P. The invention comprises computing the first two stages of an N-point FFT/IFFT as

a single radix-4 butterfly computation operation while implementing the remaining $(\log_2 N - 2)$ stages as radix-2 operations, fusing the 3 main nested loops of each radix-2 butterfly stage into a single radix-2 butterfly computation loop, and distributing the computation of the butterflies in each stage such that each processor computes an equal number of complete butterfly calculations thereby eliminating data interdependency in the stage.

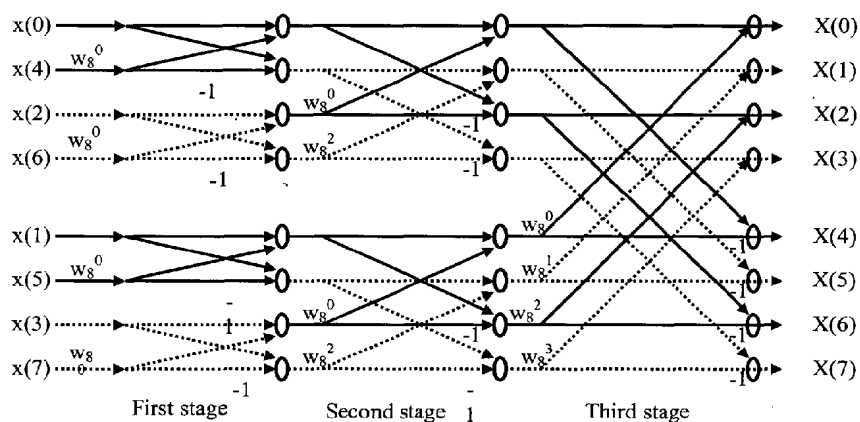


Figure 2. Butterfly distribution for 2-processor configuration



DOCUMENTS CONSIDERED TO BE RELEVANT			
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The present search report has been drawn up for all claims			
Place of search Berlin		Date of completion of the search 23 December 2005	Examiner Domingo Vecchioni, M
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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EP 03 02 7181

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82